

AMENDMENT

U.S. Appln. No. 09/840,815

NL000229

circuit switched the device to the first and second state in a test operating mode only.

4. (Amended) The electronic circuit according to Claim 2, further comprising a circuit with a power supply input terminal, the power supply input terminal being coupled to a power supply source via the main current channels of the first and second transistor.

REMARKS

Reconsideration and allowance of this application are respectfully requested in light of the above amendments and the following remarks.

SUMMARY OF OBJECTIONS:

The title was objected to as not being descriptive. The title has been amended in response to the objection.

The drawings were objected to because a legend is needed for reference number 10. Applicants propose to amend Fig. 1 to add the legend as shown in the attached drawing sheet.

In view of the above amendments, withdrawal of these objections is respectfully requested.

SUMMARY OF THE REJECTIONS:

(1) Claims 2-4¹ stand rejected under 35 U.S.C. 112, second paragraph, as being indefinite.

(2) Claims 2-4 stand rejected under 35 U.S.C. §102 as allegedly being anticipated by lee (U.S. 5,107,208 hereafter "Lee").

RESPONSE TO REJECTIONS:

First, it is noted that the claims have been amended to address the rejections based upon 35 U.S.C 112, second paragraph.

Applicants have deleted the phrase "package," clarified the configuration of the control circuit, and changed "power supply input" to --power supply input terminal--.

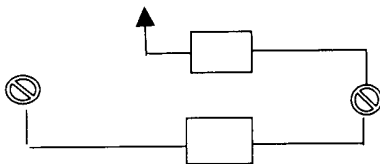
It is also respectfully submitted that Lee does not anticipate any of the instant claims at least because this reference fails to show all of the recited features of the present claims.

In particular, Claim 2 as amended is directed to an electronic circuit device including, in part, a first and second contact terminal and a first and second switching transistor each with a

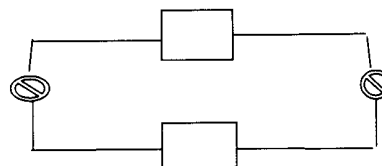
¹ It is noted that no basis for rejection Claim 1 was identified in the Office Action. Accordingly, it is believed that Claim 1 is in condition for allowance.

main current channel. The main current channels are coupled in parallel between the first and second contact terminal

The Office Action alleges Lee shows all the features of Claim 1. The Office Action states that it appears that "first and second transistors 14 and 15 have a main current channel coupled in parallel between first and second contact terminals 22 and 23. However, as shown in Fig. 1 of Lee, this is not so. By way of example, note the difference between the following configuration examples:



[Lee]



[Claim 1]

In the above drawing the rectangles represent transistors and the other symbols represent the terminals. In the configuration of Lee, the main current channels of the transistors are not in parallel as recited in Claim 2.

The Court of Appeals for the Federal Circuit held in *Verdegaal Bros. v. Union Oil Co. of California*, 814 F.2d 628,631, 2 USPQ2d 1051, 1053 (Fed. Cir. 1987):

A claim is anticipated only if each and every element as set forth in the claim is found, either expressly or inherently described, in a single

AMENDMENT

U.S. Appln. No. 09/840,815

NL000229

prior art reference.

Reconsideration and withdrawal of this ground of rejection are respectfully requested.

All claims dependent from Claim 2 are believed to be allowable at least for dependency therefrom, and for separate reasons of patentability.


CONCLUSION

For all the foregoing reasons, it is respectfully submitted that all the present claims are patentable in view of the cited references. A Notice of Allowance is respectfully requested.

Should the Examiner deem that there are any issues which may be best resolved by telephone communication, he is respectfully requested to telephone Applicants undersigned Attorney at the number listed below.

Respectfully submitted,
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Date: 10/23/02


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AMENDMENT

U.S. Appln. No. 09/840,815

NL000229

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: CORNELIS OENE CIRKEL ET AL.
SERIAL NO.: 09/840,815 EXAMINER: V. NGUYEN
FILED: April 24, 2001 ART UNIT: 2829
FOR: ELECTRONIC CIRCUIT DEVICE WITH A SHORT CIRCUIT SWITCH
AND METHOD OF TESTING SUCH A DEVICE

VERSION WITH MARKINGS SHOWING CHANGES MADE

Assistant Commissioner for Patents
Washington, DC 20231

Dear Sir:

In response to the Office Action dated September 26, 2002, the Applicant requests amendment of the above-identified application as follows:

IN THE TITLE:

ELECTRONIC CIRCUIT DEVICE WITH A SHORT CIRCUIT SWITCH
USING TRANSISTORS AND METHOD OF TESTING SUCH A DEVICE

IN THE CLAIMS:

2. (Amended) ~~EA~~An electronic circuit device
comprising:
~~a package with~~ a first and second contact terminal;

AMENDMENT

U.S. Appln. No. 09/840,815

NL000229

— a first and second switching transistor each with a main current channel, the main current channels being coupled in parallel between the first and second contact terminal, both for substantially providing a switchable short-circuit between the first and second contact terminal though the main current channels in a normal operating mode;

— a control circuit, the control circuit being arranged to switch the ~~electronic circuit~~ first and second switching transistors between at least three states, the first and second transistor being switched on and off respectively in the first state and vice versa in the second state, both transistors being switched on in the third state.

3. (Amended) ~~E~~The electronic circuit device, according to Claim 2, wherein the control circuit being switchable between the normal operating mode and a test operating mode, the control circuit switched the device to the first and second state in a test operating mode only.

4. (Amended) ~~E~~The electronic circuit according to Claim 2, further comprising a circuit with a power supply input terminal and an electronic circuit device as claimed in claim 2, the power supply input terminal being coupled to a power supply source via the main current channels of the first and second transistor.